

FACULTY PROFILE

ROHITH BALA JASWANTH.B

Department: Electronics and Communication Engineering

College: Gudlavalleru Engineering College

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Address for Communication

B.R.B. Jaswanth

S/o B.Sree Ramulu, D.No:2/302,

Bantumilli Road,

Gudivada

Krishna (District)-521301

Academic Qualifications

S. No.	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	M.Tech	J.N.T.U.K Kakinada	73	VLSI design	2012
2	B.Tech	J.N.T.U.K Kakinada	74.65	Electronics and Communication Engineering	2008
3	Intermediate	State Board of Intermediate Education	86.4	M.P.C	2004
4	S.S.C	State Board of Secondary education	81.66	---	2002

Professional Experience

S. No.	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	20-05-2013	Till date
2	Assistant Professor	V K R, V N B and A G K College of Engineering, Gudivada	18-06-2012	01-05-2013

Professional Body Membership

-- AMIEI – AM156033-9

Papers Published in Journals No. : -- 15

PUBLICATIONS

- M.Nagu, B.RajaRao, B.R.B.Jaswanth, "Morphological Text Localization using wavelet and neural network", International Journal of Computer Science and Information Technologies (IJCSIT),Vol.2, 0975-9646 ,pp, November 2011.Impact Factor-1.5.
- B.R.B.Jaswanth, R.V.S.Rayudu, K.Manibabu, "A Design of Low Power Delay Buffer", International Journal of Technological Exploration and Learning (IJTEL),Vol.2,Issue 2. April 2013,pp,Impact factor-1.2.
- Ch.Sindhura, B.R.B.Jaswanth, "Implementation of Modern Vigilance Control Device using GSM & GPS Technology" in International Journal of Latest Trends in Engineering and Technology (IJLTET), Vol.2,Issue.4.,2278-621X,pp,July 2013,Impact Factor-1.6.
- Ch.Sindhura, B.R.B.Jaswanth," Designing of a Low Cost Based Alerting System to Prevent Train Accidents Using GSM & GPS Technology",International Journal of Engineering Research & Applications (IJERA), 2248-9622.Vol.4,Issue 3. pp, July-Aug 2013,Impact factor-1.6
- B.R.B.Jaswanth, Muneendra, "Crack Sensing Scheme in Rail Tracking System", International Journal of Engineering Research & Applications (IJERA), Vol.4,Issue 1(Version 2), 2248-9622,pp,January-2014,Impact factor-1.6.
- N.Jhansi, B.R.B.Jaswanth, "Design and Analysis of High Performance FIR Filter using MAC Unit", International Journal of Advanced Research in Computer and Communication Engineering(IJARCCCE),Vol.3,Issue-11,8626-8629,pp,ISSN(Online): 2278-1021;ISSN(Print): 2319-5940, Certificate. No. V3I11A5A-2/2, 27th November 2014, Impact factor-2.793.
- Sasidev.R, Rohith Bala Jaswanth B," Implementation of high Security NFC Tag on FPGA", International Journal of Innovative Research in Advanced Technology(IJIRAT),Vol.2.,Issue-12, ISSN:2319-1467,02nd December,2015
- Madda Ramya Krsihna, Rohith Bala Jaswanth B," **Performance Evaluation of Adaptive Hold Logic Based Algorithmic Implementation in Multipliers**", International Journal of Applied Sciences, Engineering and Management (IJAEM),

- Rohith Bala Jaswanth B, V.V.K.D.V.Prasad, M.Kamaraju,"**A Survey of Operational Transconductance Amplifier and its Techniques in CMOS Technology,**" International Journal of Advance Engineering and Research Development (IJAERD), UGC approved,Volume.04,Issue-12December 2017,DOI:10.21090/IJAERD, ISSN (Online) : 2348-4470, ISBN : 2348-6406,Impact Factor:4.72(SJIF-2016)
- C Devi Supraja, E. Vijaya Babu, Rohith Bala Jaswanth B, Papa rao Nalajala,Bhavana Godavarthi” **Design and Analysis of CNFET based 2:1 MUX in Nano Scale Region”, Journal of Advanced Research in dynamical & Control systems(JARDCS)- a special Issue on Environment, Engineering and Energy ”** Vol.9.,Sp-14/March 2017, pp.2069-2077(Scopus indexed Journal), ISSN: 1943-023X
- Rohith Bala Jaswanth B, V.V.K.D.V.Prasad, M.Kamaraju, “Design of Three Stage Cascaded Operational Transconductance Amplifier for ECG Applications”, Multimedia Tools & applications- Springer, SCi journal, DOI:10.1007/s11042-018-6873-9, 8 November 2018, Springer Science+Business Media, Springer Nature 2018
- Rohith Bala Jaswanth B, V.V.K.D.V.Prasad, M.Kamaraju “Impact on Gain and Noise : Novel Modeling Approach for OTA Based ECG Amplifier”, International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems-2018 (CITAES-2018), Nov 30th- Dec 01st , 2018 at Gudlavalleru Engineering College, Gudlavalleru
- B.R.B. Jaswanth, I. Priyanka, G. Chaitanya, J. Srinadh, T. Siva Ganesh,” Pi Cam Based Smoke/Fire Alerting System,” **International Research Journal of Engineering and Technology (IRJET)** Vol. 6, Issue 4, April 2019, e-ISSN: 2395-0056, p-ISSN: 2395-0072, Impact Factor value: 7.211, ISO 9001:2008 Certified Journal,pp.2324-2329
- B R B Jaswanth, G Santhosh , K.Siva Kalyan, G.Siva Balaji, “A Comparative Analysis of CMOS amplifiers for ECG Signals”, **International Research Journal of Engineering and Technology (IRJET)** Vol. 6, Issue 4, April 2019, e-ISSN: 2395-0056, p-ISSN: 2395-0072, Impact Factor value: 7.211, ISO 9001:2008 Certified Journal,pp.1079-1087
- Rohith Bala Jaswanth B, V.V.K.D.V.Prasad, M.Kamaraju, ” Design of Operational Transconductance Amplifier (OTA) using Heterogeneous Avalanche Voltage Changeover Circuit”, **Journal of Advanced Research in dynamical & Control systems(JARDCS)- a special Issue on Environment, Engineering and Energy ”** Vol.12.,Issue.02,May 2020, DOI: 10.5373/JARDCS/V12I2/S20201293 , pp.2455-2472(Scopus indexed Journal), ISSN: 1943-023X

DETAILS OF PATENTS PUBLISHED:

1. TITLE OF INVENTION : PREVENTING VEHICLE ACCIDENTS: INTELLIGENT ACCIDENT AVOIDANCE USING SATELLITE-BASED POSITIONING SYSTEM.

(a) APPLICATION NUMBER : 202041028920

(b) APPLICATION TYPE :ORDINARY

(c) APPLICATION DATE OF FILING :07/07/2020

(d) APPLICANT NAME : 1 . Dr. VVKDV PRASAD (PROFESSOR AND HEAD)

2. Dr. RANGAIAH LEBURU (PROFESSOR AND HEAD)

3. Dr. SHAIK. MAHABOOB BASHA (PROFESSOR)

4. Mr. ROHITH BALA JASWANTH B (ASSISTANT PROFESSOR)

5 . B. VAMSY KRISHNA (ASSISTANT PROFESSOR)

(e) FIELD OF INVENTION: COMMUNICATION

(f) PUBLICATION DATE (U/S 11A) : 31/07/2020

ONLINE WORKSHOPS ATTENDED:

1. Participated in Five Day Online faculty development Program on Innovative trends in data Analysis with AI from 26-3-2020 to 30-3-2020 presented by Codegnan IT Solutions in association with Department of ECE , Malineni Lakshmaiah Women's Engineering College , Guntur
2. Participated in NAAC Awareness Programme for Faculty" organized by Marathwada Mitra Mandal Institute of Technology (MMIT), Lohgaon, Pune - 47 conducted online during the period 08/05/2020 to 14/05/2020.
3. Successfully completed online Faculty Program on NBA on 12/5/2020, with a passing score of 80% organized by Bharati Vidyapeeth College Of Engineering, Navi Mumbai
4. Successfully attended Leadership Talk With Dr. Pramod Chaudhari, Founder, Chairman, Praj Industries Limited and Dr. Abhay Jere, Chief Innovation Officer MHRD Innovation Cell, Saturday, 16th May 2020 at 01.00 PM by MHRD's Innovation Cell

5. Participated in the “Faculty Awareness Programme on NBA to keep in touch with NBA Accreditation” organised by Internal Quality Assurance Cell(IQAC), PACE Institute of Technology and Sciences, Ongole on 17/5/2020.
6. Participated in an online MATLAB skill test National Level Quiz Organized by Department of ECE , Raghu Engineering College, Visakhapatnam during 15-5-2020 to 17-5-2020
7. Participated and secured 100 % score in an Online Quiz on Circuit Theory conducted by Department of EEE, Muthayammal Engineering Institutions, Tamil Nadu on 19-5-2020 & 20-5-2020.
8. Participated in Six Day National Level FDP on Artificial Intelligence and Applications organized by Department of CSE, in collaboration with 360DigiTMG from 25-5-2020 to 30-5-2020.
9. Participated in the webinar on “RESEARCH MEASUREMENTS AND PARAMETERS FOR FACULTY PUBLICATIONS”, on 30/05/2020 Organized by Department of ECE,NRI Institute of Technology, Agiripalli.
10. Participated in Online Quiz on Electronic Science Quiz organized by Department of Electronics & Communication Engineering , Brilliant Grammar School Educational Society’s Group or Institution’s-Integrated Campus, Hyderabad on 31-5-2020
11. Participated in Online Quiz on Research Methodology and secured 100% on 31-5-2020 organized by Department of CSE , Padmabhooshan Vasantraododa Patil Institute of Technology , Budhgoan
12. Participated in Online National Level Quiz on Python Programming and stood among top 15% by Scoring 75 / 75 conducted on 31/5/2020 Organized by IndianServers,Vijayawada.
13. Participated in a One Day Webinar on Artificial Intelligence on 31-5-2020 organized by Department of IT , Malineni Lakshmaiah Women’s Engineering College , Guntur
14. Participated in Online ENGLISH e-Quiz on Articles organized by Department of BASIC SCIENCE & HUMANITIES, VKR,VNB & AGK College of Engineering , Gudivada –May 2020
15. Participated in One Week International Knowledge Development Program on Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: Researcher’s View organized by Department of ECE, Gudlavalleru Engineering College from 8-6-2020 to 13-6-2020.

16. Participated in Three Day National Level FDP on Recent Advancements in Image Processing & IoT organized by Department of ECE, Lingayas Institute of Management & Technology from 3-7-2020 to 5-7-2020.
17. Participated in One Week National level Online Faculty Development Program on Cyber Security organized by Department of CSE & IT in association with Cyberspy organized by Department of ECE, Gudlavalleru Engineering College from 22-7-2020 to 26-7-2020.
18. Participated in Online Quiz on Social awareness on COVID-19 organized by University College of Engineering, JNTUK,Vizianagaram
19. Participated an Online quiz on MATLAB skill test National Level Quiz Organized by Department of ECE , Raghu Engineering College, Visakhapatnam during 15-5-2020 to 17-5-2020.
20. Participated an Online Webinar on Impact of COVID in Engineering Education and way forward organized by Indian Society for Technical Education (ISTE) , Gudlavalleru Engineering College, Gudlavalleru on 9-6-2020
21. Participated in an Online quiz on Research Methodology organized by R&D Cell V. V. Institute of Pharmaceutical Sciences, Gudlavalleru

Papers Published in Conferences No. : 1

- Rohith Bala Jaswanth B, V.V.K.D.V.Prasad, M.Kamaraju “Impact on Gain and Noise : Novel Modeling Approach for OTA Based ECG Amplifier”, International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems-2018 (CITAES-2018), Nov 30th- Dec 01st , 2018 at Gudlavalleru Engineering College, Gudlavalleru

Workshops /Conferences /Seminars Attended No. : 12

- Attended a two day workshop and hands-on-session on “FPGA based DSP Implementation using Xilinx” during 26th-27th November 2010 at GMRIT, Rajam.
- Attended a national conference on “Emerging Technologies in Electronics & Communications NCETEC-2K11” and presented a paper entitled “Circuit Design of Low Power Delay Buffer” on 3rd &4th December,2011 in Vishnu Sree Institute of Technology, Nalgonda, Hyderabad.
- Attended a two day national workshop on “VLSI” at V.K.R., V.N.B.& A.G.K. College of

Engineering on 22nd & 23rd February 2013 in collaboration with PGP Electronics Private Limited, Hyderabad.

- Attended a two day workshop on “Issues in Integrated Circuits Design” held during 13th-14th December,2013 organised by the department of ECE, Gudlavalleru Engineering College.
- Attended a Three Day National Workshop on “ Theory & Applications of Intelligent Signal Processing” organized by Signal Processing and Communication Research group, ECE Dept., during 28th February to 2nd March, 2014.
- Attended a two week ISTE workshop on “Signals & Systems” conducted by IIT, Kharagpur from 2nd-12th January,2014 in Gudlavalleru Engineering College.
- Attended a two week ISTE workshop on “Analog Electronics” conducted by IIT, Kharagpur from 17th -23rd June,2014 in Gudlavalleru Engineering College.
- Attended a workshop on “Ultra Low Power Communication using CC430 Microcontroller-Hands-on-Session” in Institution of Engineers office, Vijayawada on 7th February, 2015.
- Attended a One Day Workshop on Research in Energy Management Wireless Networks & Smart Transportation Systems organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada on 29th March, 2015.
- Attended a workshop on “Speech Processing: Current Challenges and Hands-on-Session” in Gudlavalleru Engineering College.
- Attended a workshop on “Research and Development Facilitation” in Institution of Engineers office, Vijayawada.
- Attended a Two Day All India Workshop on “Emerging Techniques on VLSI Architecture Design for Real Time Environment” organized by The Institute of Engineers(India) conducted by the Department of ECE, PACE Institute of Technology & Sciences, Ongole during 09th-10th October,2015.
- Attended a Fifteen day Faculty Development Program in Aachen, Germany on Applied Robotics Control during 19-5-2019 to 2-6-2019

Certifications/Training Programs Attended No. : 6

- Attended one week refresher course in Gudlavalleru Engineering College on the subject “Electronic Devices and Circuits” during 23rd-28th September, 2013.
- Attended a Two Week Audit Course on “Research Methodologies” during 1st to 15th May, 2015 at JNTUK, Kakinada.

- Attended a Two Week Audit Course on “Intellectual Property Rights and Patents” during 16th to 30th May, 2015 at JNTUK, Kakinada.
- Attended a Two Day Faculty Development Programme(FDP) on “Advanced Digital Signal Processing & Applications(ADSP-A)” sponsored by Siddhartha Academy of General & Technical Education (SAGTE) and TEQIP II (S.C.1.2) held on 12th -13th September,2015 organized by ECE Dept., V.R.Siddhartha Engineering College, Vijayawada, Andhra Pradesh.
- Attended a One Week Faculty Development Programme (FDP) on “Development of Wireless Sensor Networks using Network Simulator(NS2)-Hands on experience “ organized Communication Research Group in Association with Vertex Soft, Hyderabad during 01st to 05th December, 2015.
- Attended a ten day faculty development Programme on “Aspects of IC design” at National Institute of Technology (NIT), Warangal during 08th - 17th February, 2015.

Guest Lectures Delivered No. : 1

Delivered a guest lecture on refresher course, Embedded System Design to the faculty of ECE, Gudlavalleru Engineering College, Gudlavalleru.

Subjects Handled

Under Graduation

- Bio-Medical Engineering
- Computer Networks
- Satellite Communications
- Principles of VLSI Design
- Embedded System Design
- Microwave & Optical Communications
- Linear Integrated Circuits & Applications
- Pulse Circuits
- Probability Theory and Random Variables

Post Graduation

-- Internet of Things

Labs Handled

Under Graduation

- Electronic devices and Circuits
- Linear IC applications
- Analog Circuits
- Microwave and optical Communications

Other Responsibilities

College Level (in GEC) -- One of the members of Press and Media Committee for the Annual Program GEC fest celebrated every year.

Department Level (in GEC)

- Academic Coordinator of Final year ECE for the academic year 2016-2017
- NBA Coordinator of I year from 2014 till now.
- Held the role of Class Incharge for the academic years 2013 to 2016 for various classes of II,III & IV B Tech
- Held the role of Project Coordinator of one of the classes of IV B Tech ECE for 2013 to 2016.
- Held the role of Mini Project Coordinator of one of the classes of III B Tech ECE for 2016 to 2018.
- Held the role of Mini Project Coordinator of one of the classes of III B Tech ECE for 2019 to 2020
- ARC Coordinator of ECE
- Press & Media Coordinator of the department.

Projects Guided

Under Graduation --02

S. No.	Name of the project	Year
1	Solar Irrigation System	2011
2	Low Power delay Buffer using Gated Driver Tree	2012
3	Power Optimized Remote Controlled DC Operable Robot for Border Security	2014
4	Thermo Electric Refrigerator	2016
5.	Jewellery Aegis	2017
6.	Pi Cam Based Smoke/Fire Alerting System	2019
7.	A Comparative Analysis of CMOS amplifiers for ECG Signals	2019
8.	Smart School Monitoring System	2020
9.	Evaluation of OTA amplifier using EEG Signals	2020

S. No.	Name of the project	Year
1	Implementation of modern vigilance Control device using GSM and GPS Technology	2013
2	Crack sensing Scheme in Rail Tracking System	2014
3	Design and Analysis of High Performance FIR filter using MAC Unit	2015
4	Implementation of High Security NFC Tag on FPGA	2016

Guiding Ph. D Students -- **NIL**

Books Published No. : -- **02**

1. Co-author of book entitled "Electronics and Communication Engineering laboratory Manual" with ISBN – 978-1-4276-5536-3.
2. Co-author of book entitled "Electronics and Communication Engineering Instructional Material" with ISBN – 978-1-4276-5539-4.

R&D and Consultancy

S. No.	Project Title	Source of Funding	Duration	Role	Status
1	Design and Development of Energy Efficient System for Control of Street Lighting and Vehicle Detection using Wireless Sensor Networks and Actuators	University Grants Commission	2 Years	Principle Investigator	Submitted on 20 th August 2015

Curriculum Design and Development

- Computer Networks
- ARM System on Chip
- Digital Communication Lab Manual

Academic Rewards / Achievements -- got registered for PhD in the year 2014 in JNTUK,kakinada

Extra Curricular Activities

- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.
- NBA Criteria- V (Faculty Contributions) member

Declaration

I, hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 07-09-2020

Signature

Place: Gudlavalleru

(B.R.B.Jaswanth)